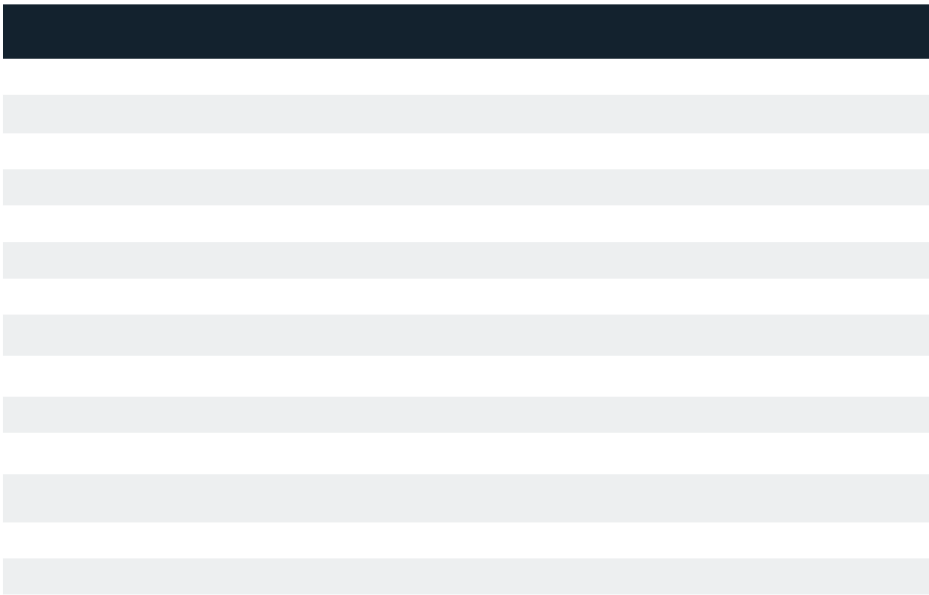
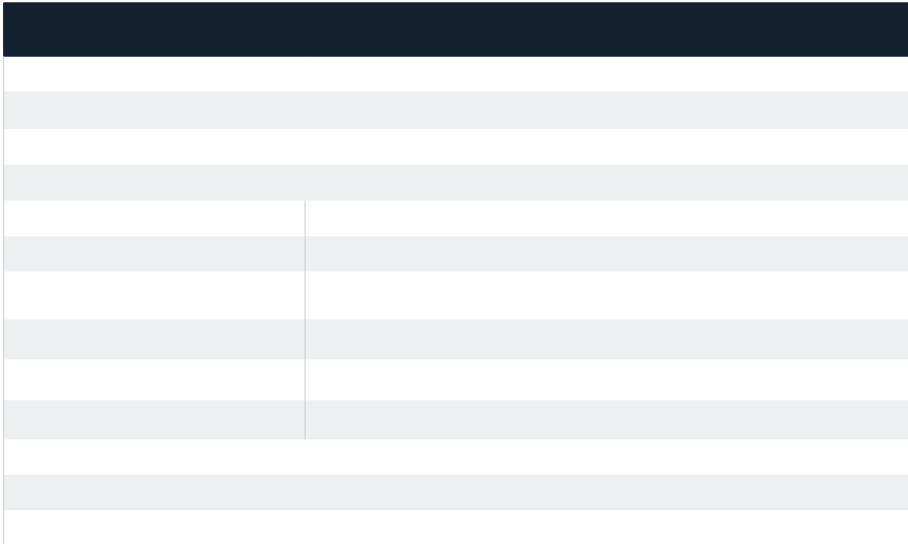


PHOENI FLIGH COMP ER



Phoenix features a fault-tolerant LEON3 SparcV8 processor based on 32-bit architecture. The processor contains fault-tolerant features built-in for surviving in harsh environments. Those features include register file SEU error-correction of up to 4 errors per 32-bit word, cache memory error-correction of up to 4 errors per tag or 32-bit word, and BCH EDAC and Reed-Solomon EDAC protection of NV-memory/SRAM and SDRAM main memory.



MODULARITY

Phoenix incorporates modular features to ensure mission flexibility and a smooth path to meeting customer needs. These features include a three-slot chassis design allowing for mission-specific peripherals, changes to the user's connector interface with minimal impact and plugin modules to accommodate upgrades and changes with no impact to other modules.

Phoenix Flight Computer

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